

FIG. 1

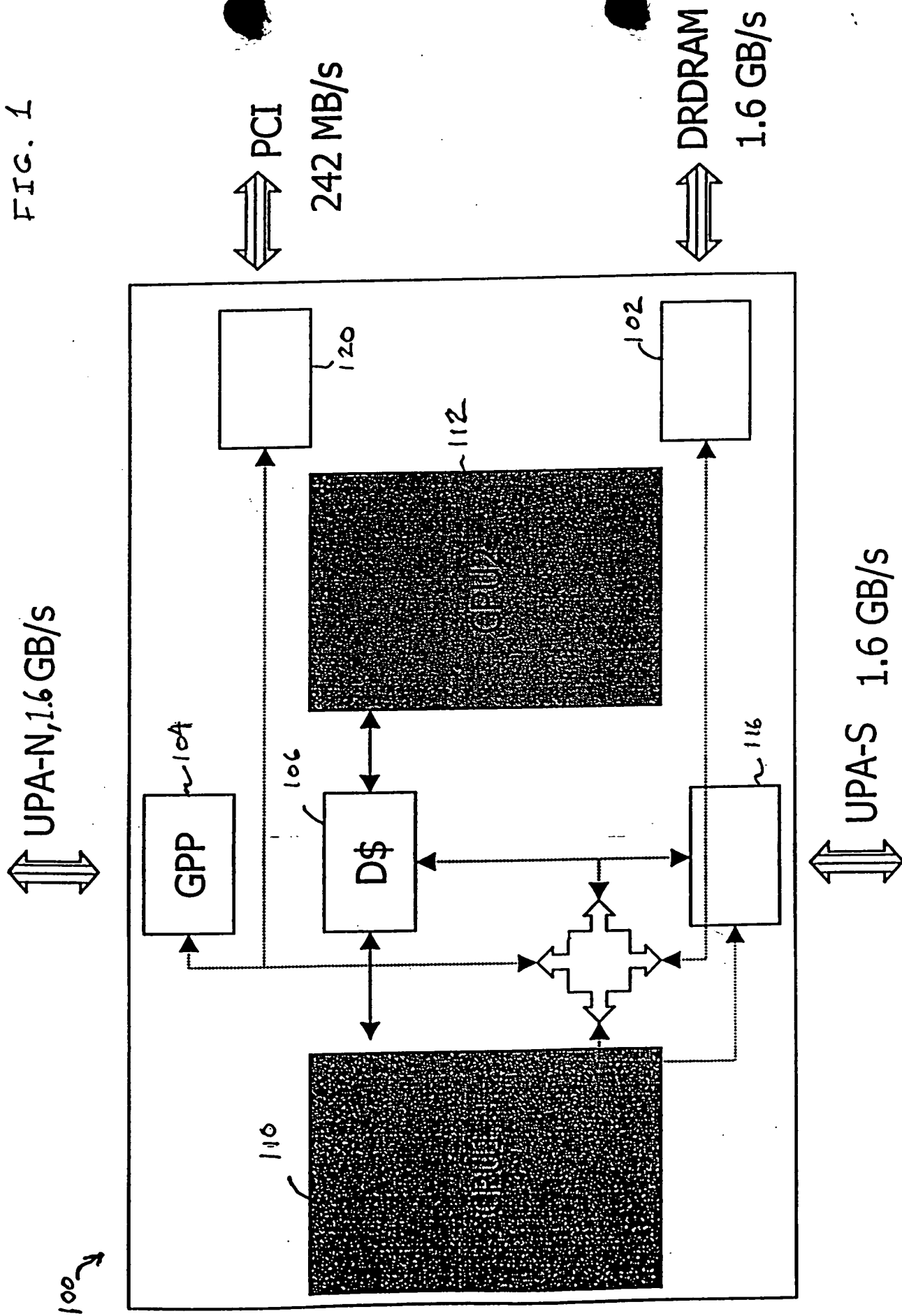
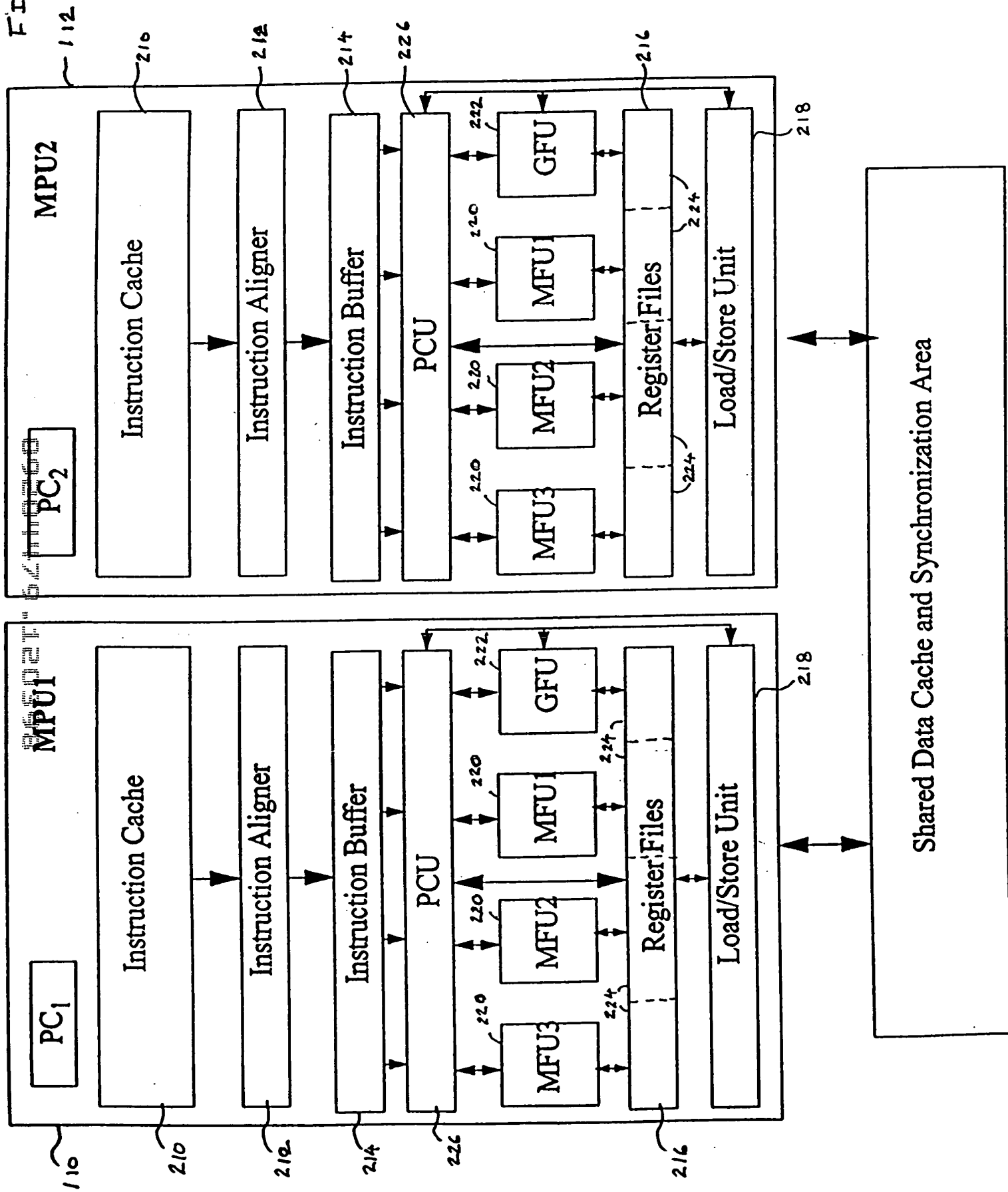


FIG. 2



216
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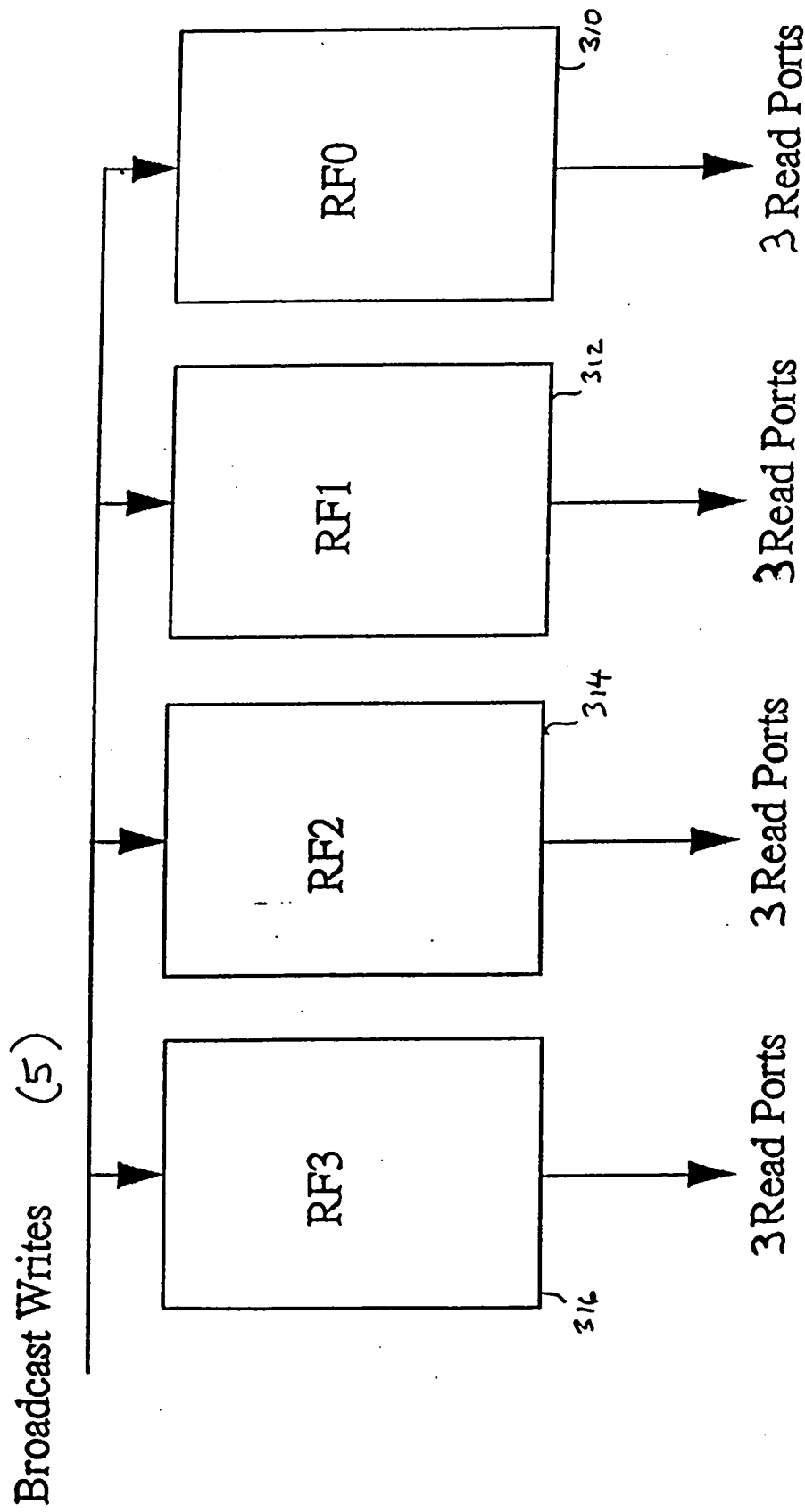


FIGURE 3 .Logical register file physically spit in four coherent parts.

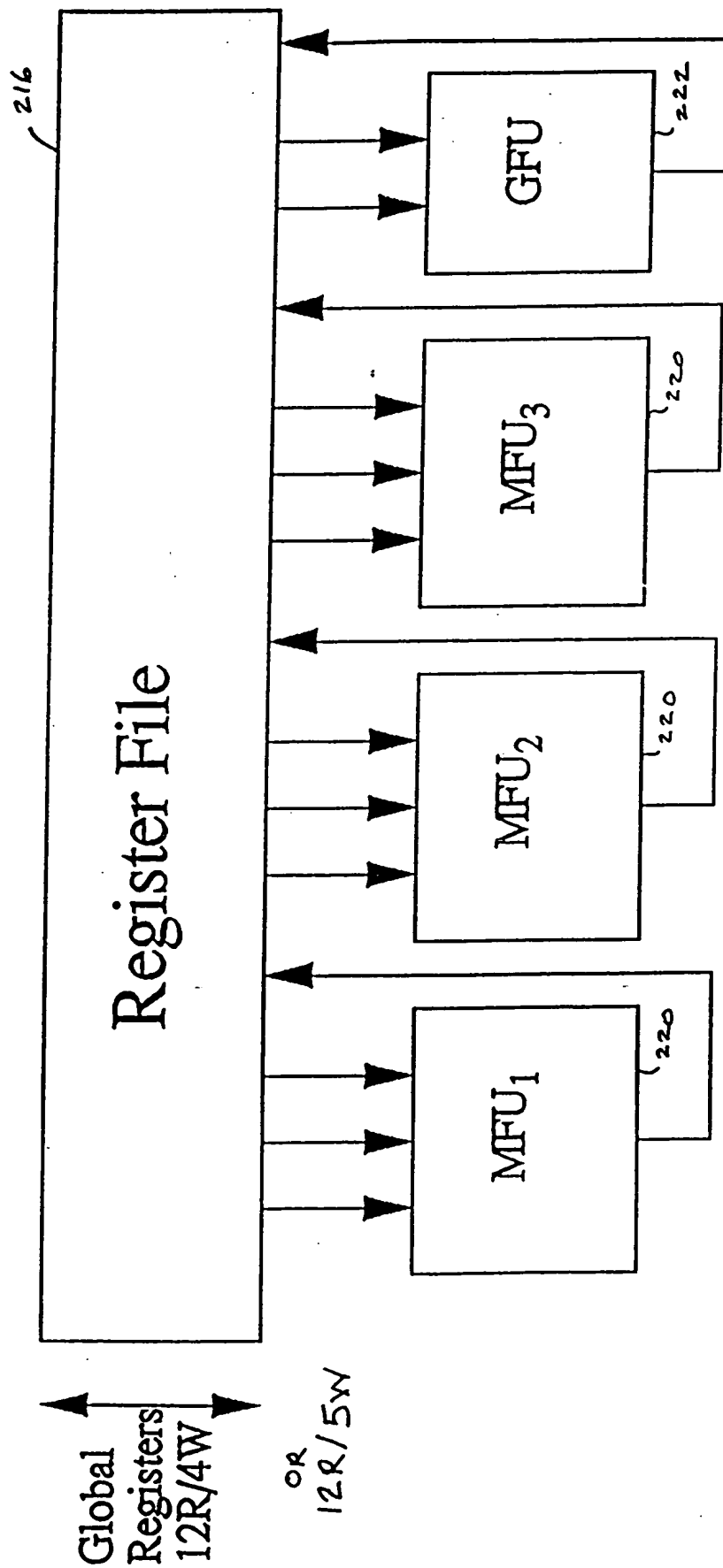
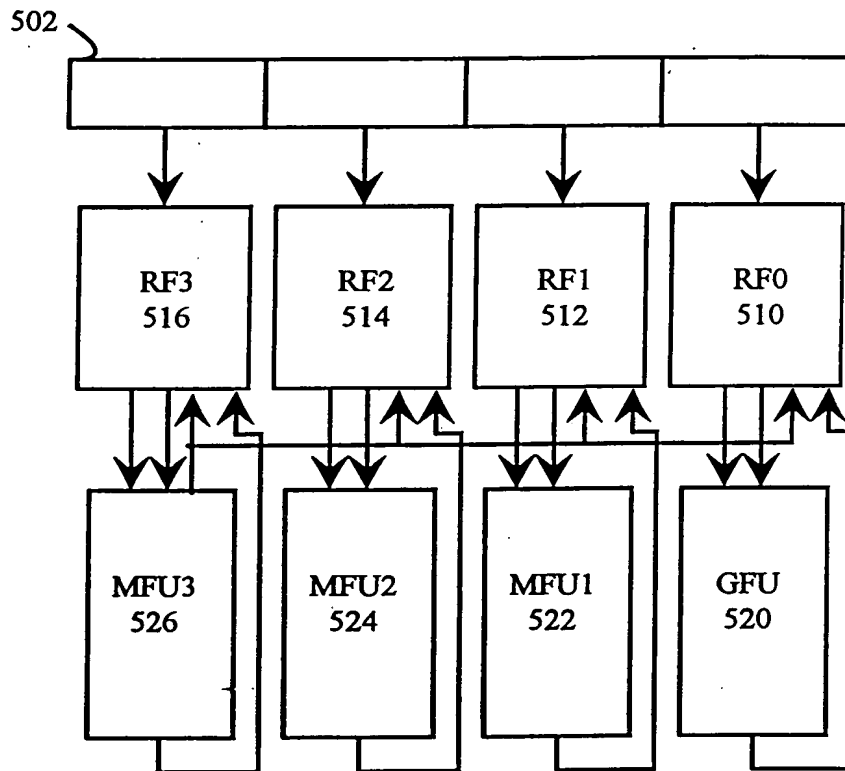


FIGURE 4 .Logical view of the register file to the compiler/programmer.

FIG. 5A



muladd Ra, Rb, Rc, Rd

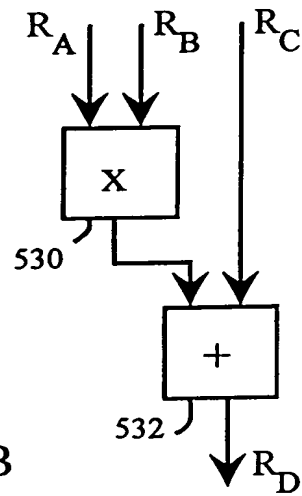


FIG. 5B

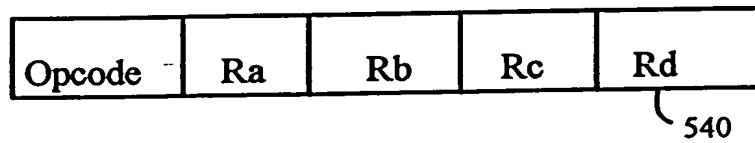
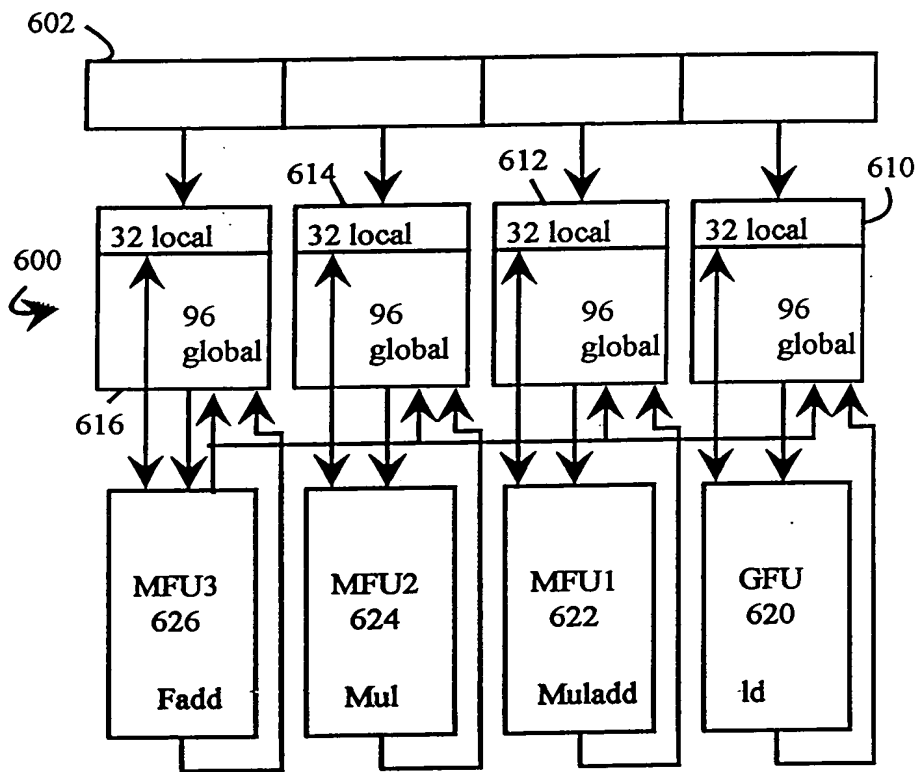


FIG. 5C

FIG. 6



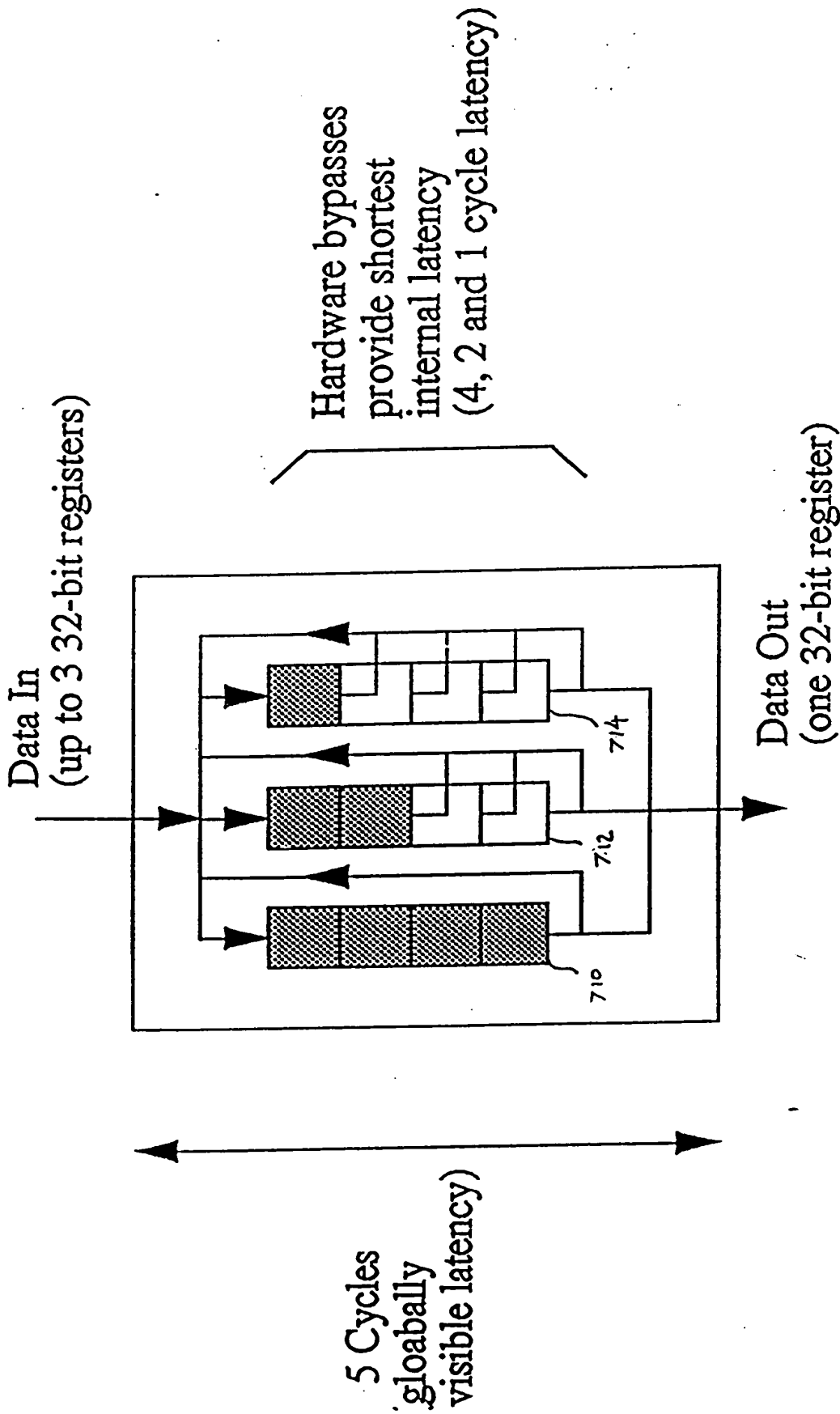


FIGURE 7 .Simplified MFU.

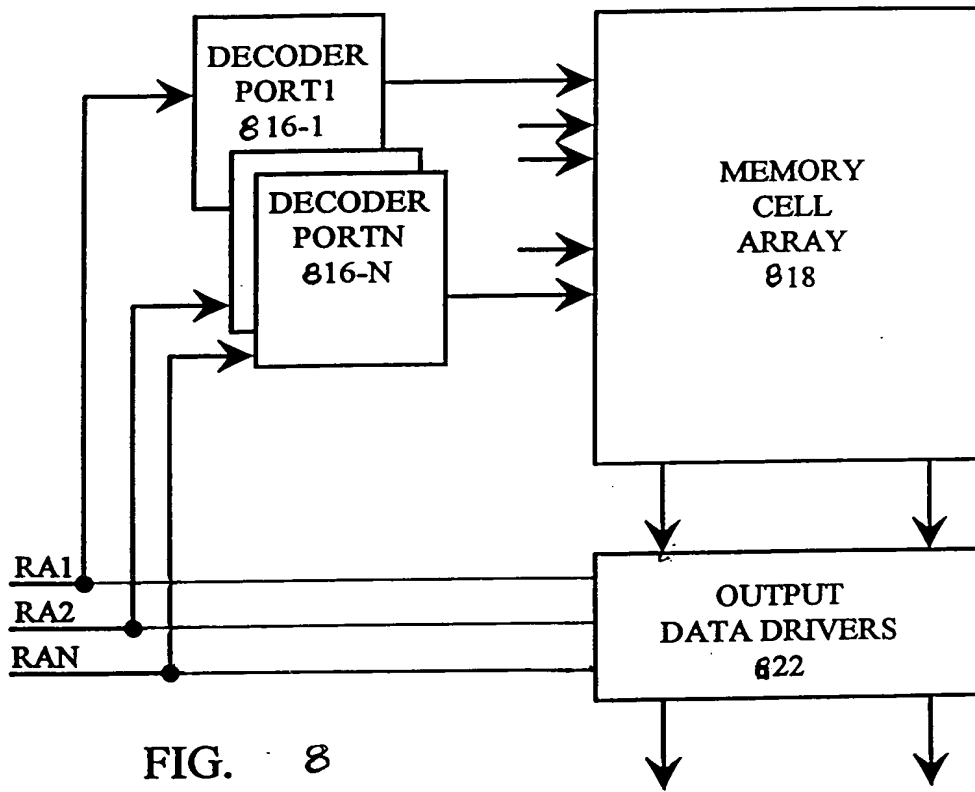
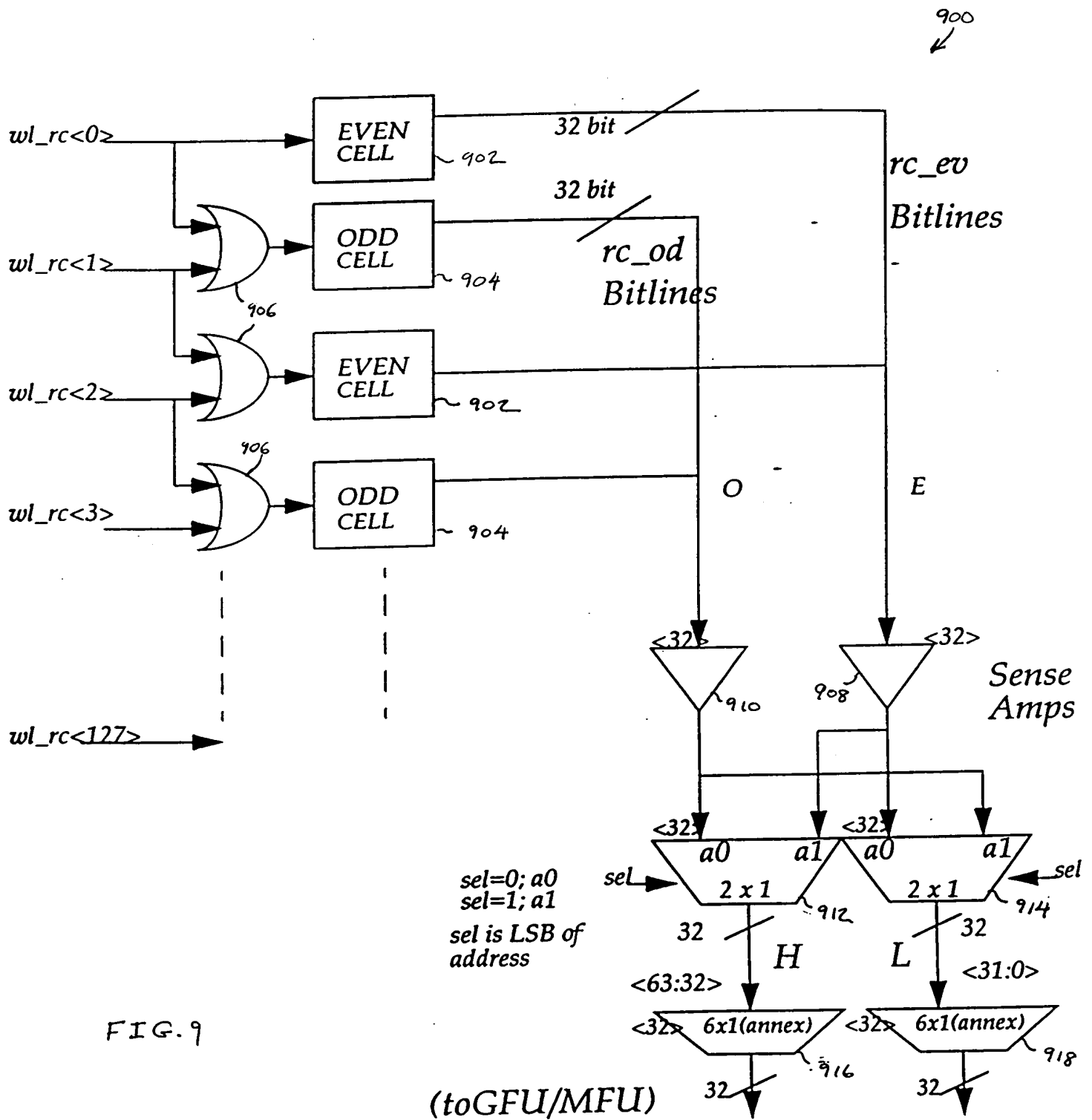


FIG. 8



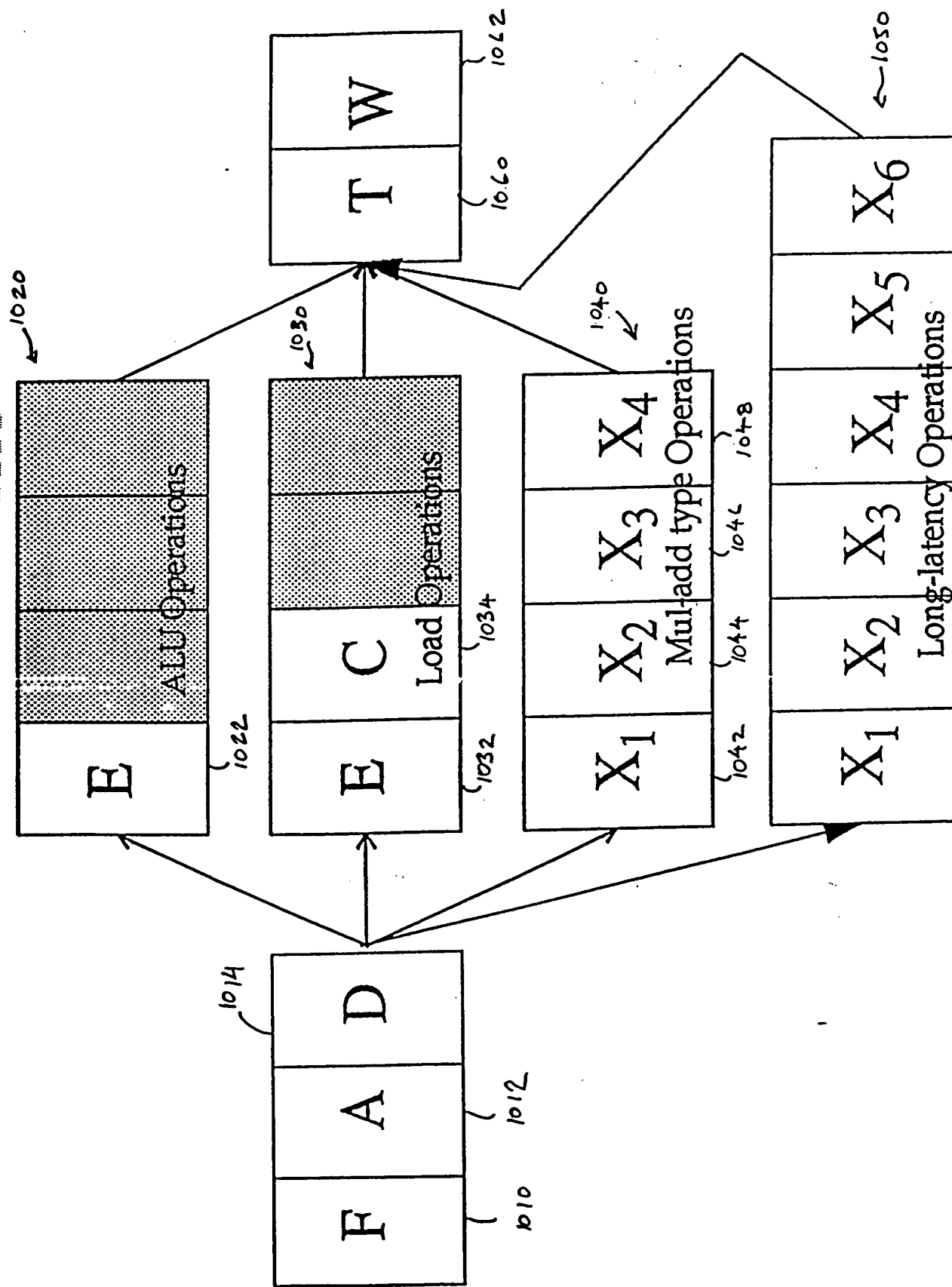


FIGURE 10 .Pipeline diagram.